



PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Brian A. Petersen and Mark A. Ross  
Assignee: Cisco Technology, Inc.  
Title: NETWORK PROCESSOR SYSTEM INCLUDING A CENTRAL  
PROCESSOR AND AT LEAST ONE PERIPHERAL PROCESSOR  
Application No.: 09/469,409 Filing Date: December 22, 1999  
Examiner: Melvin H. Pollack Group Art Unit: 2142  
Docket No.: CIS0042US Confirmation No.: 4940

Austin, Texas  
September 29, 2004

Mail Stop RCE  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR EXAMINER INTERVIEW

Dear Sir:

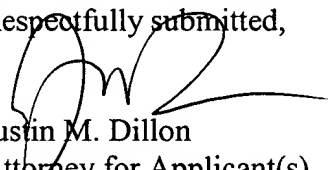
Applicants respectfully request an Examiner interview in the above-referenced case prior to the issuance of any further action. The Examiner is invited to contact Applicants' undersigned representative at his convenience to schedule the interview for a mutually convenient time.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, Virginia, 22313-1450, on June 30, 2003.

  
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Attorney for Applicant(s)

9-29-04  
\_\_\_\_\_  
Date of Signature

Respectfully submitted,

  
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